

09/914505

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Yukio KOYANAGI

S. N.

International S.N.: PCT/JP00/03040

Filed:

International Filing Date: 11 May 2000

For: INTERPOLATION CIRCUIT

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Please make the following amendments to this application
prior to examination thereof:

In the Claims:

Please amend claim 6 by replacing it with the like numbered
claim hereinbelow. A mark up of claim 6 is provided at the end
of the amendment to illustrate the changes made for the Examiner.

6. (Amended) The interpolation circuit according to
claim 1, characterized by comprising data appending unit for
appending data having the symmetrical values proportional to the
input data before and after said input data in the former stage
of said oversampling operation unit.

Add new claims 7-10 as follows:

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